



1/4

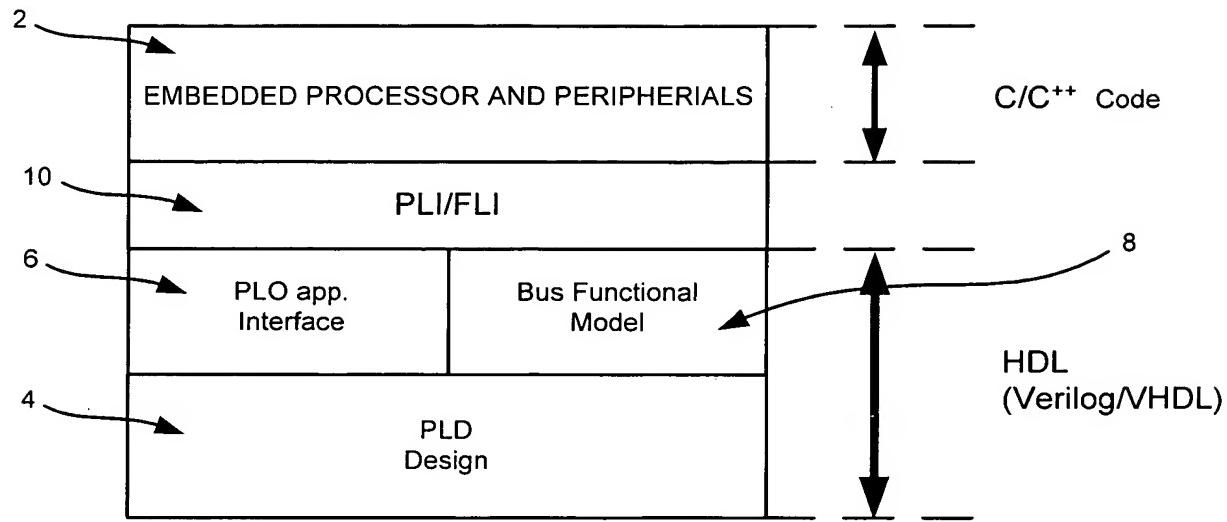


FIG. 1

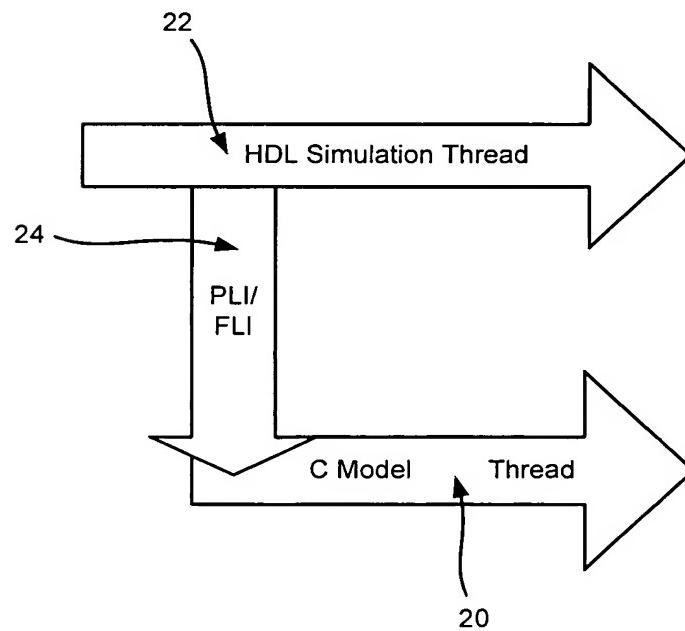


FIG. 2

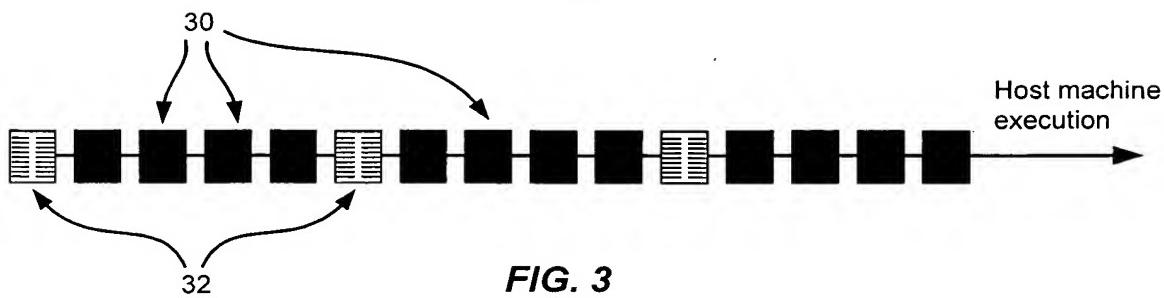


FIG. 3

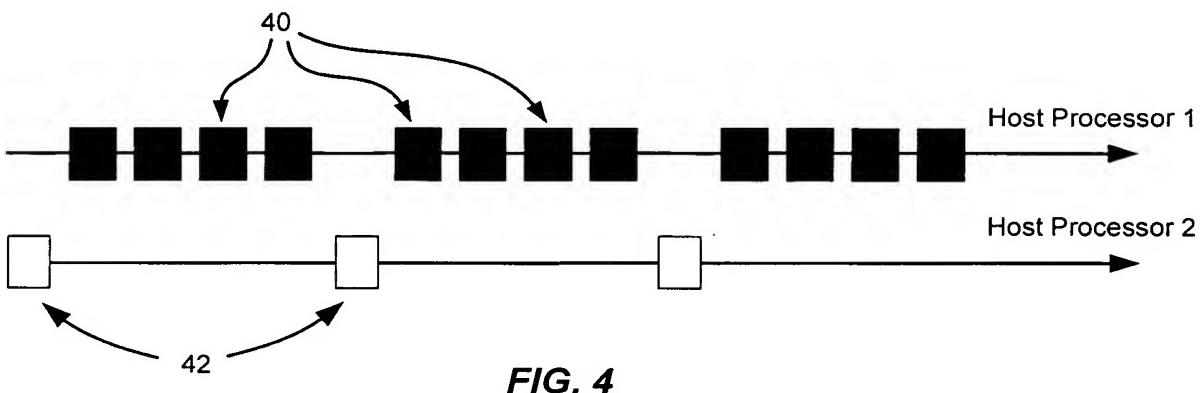


FIG. 4

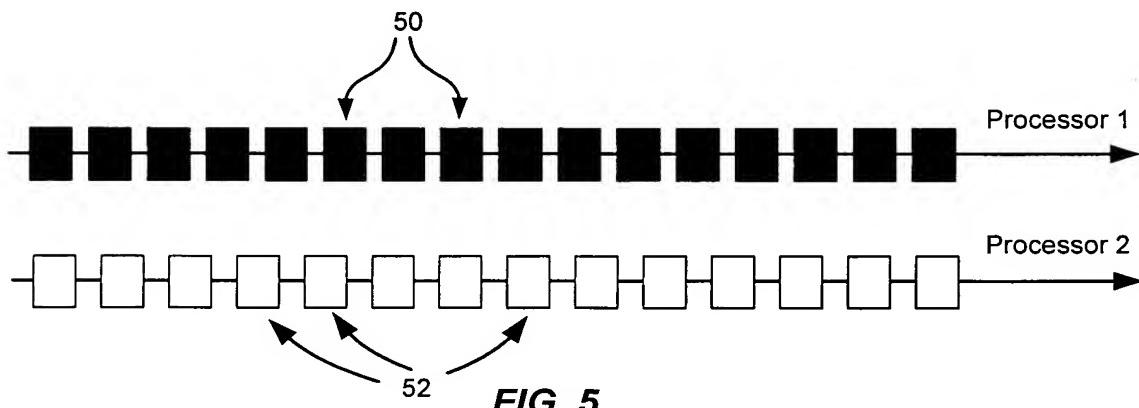


FIG. 5

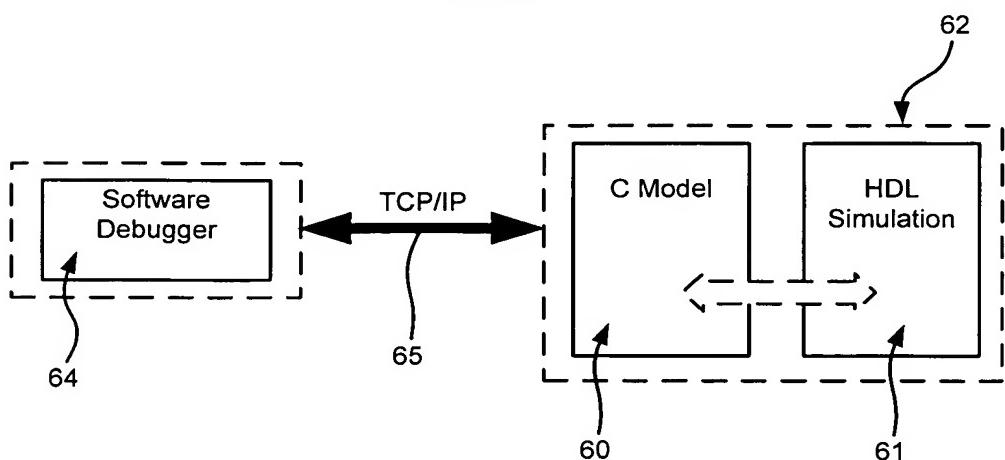
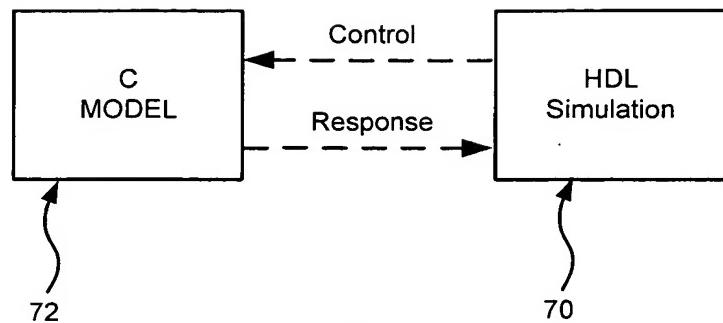
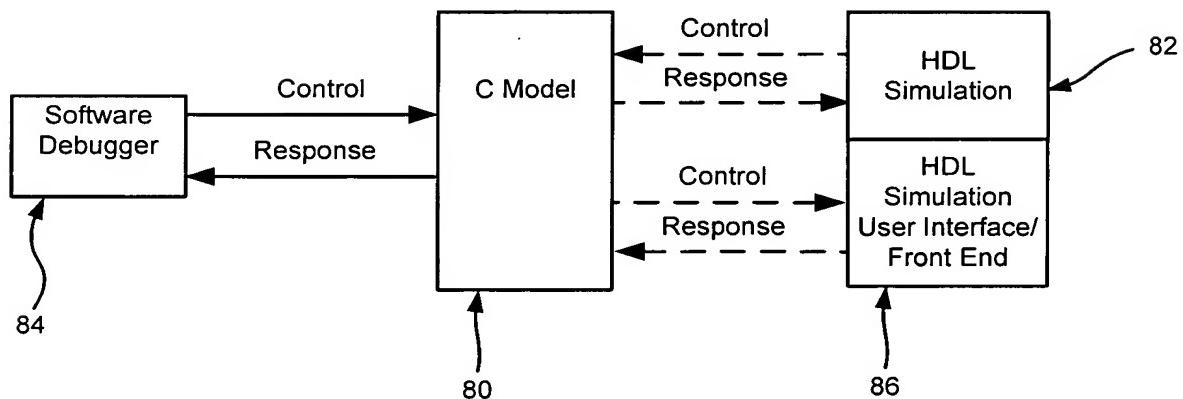
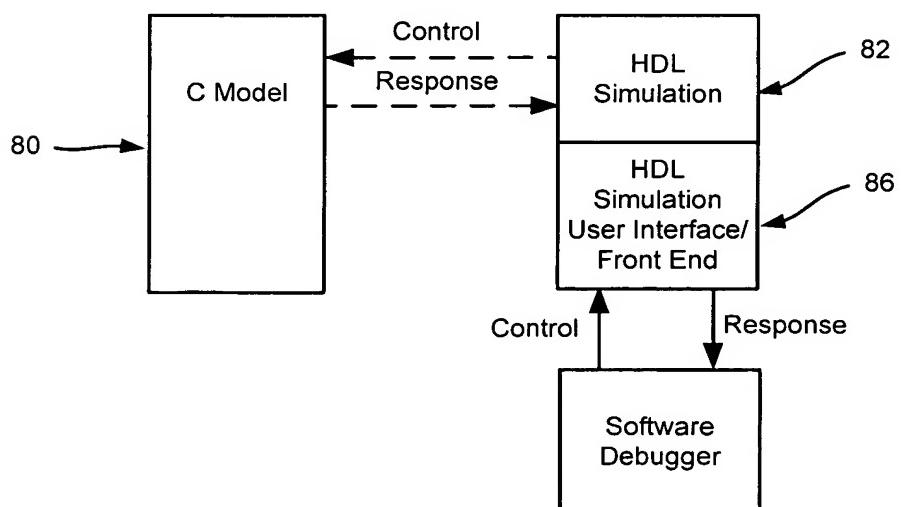


FIG. 6

**FIG. 7****FIG. 8****FIG. 9**



C model thread polls comm. thread every clock cycle for any characters in comm. thread buffer

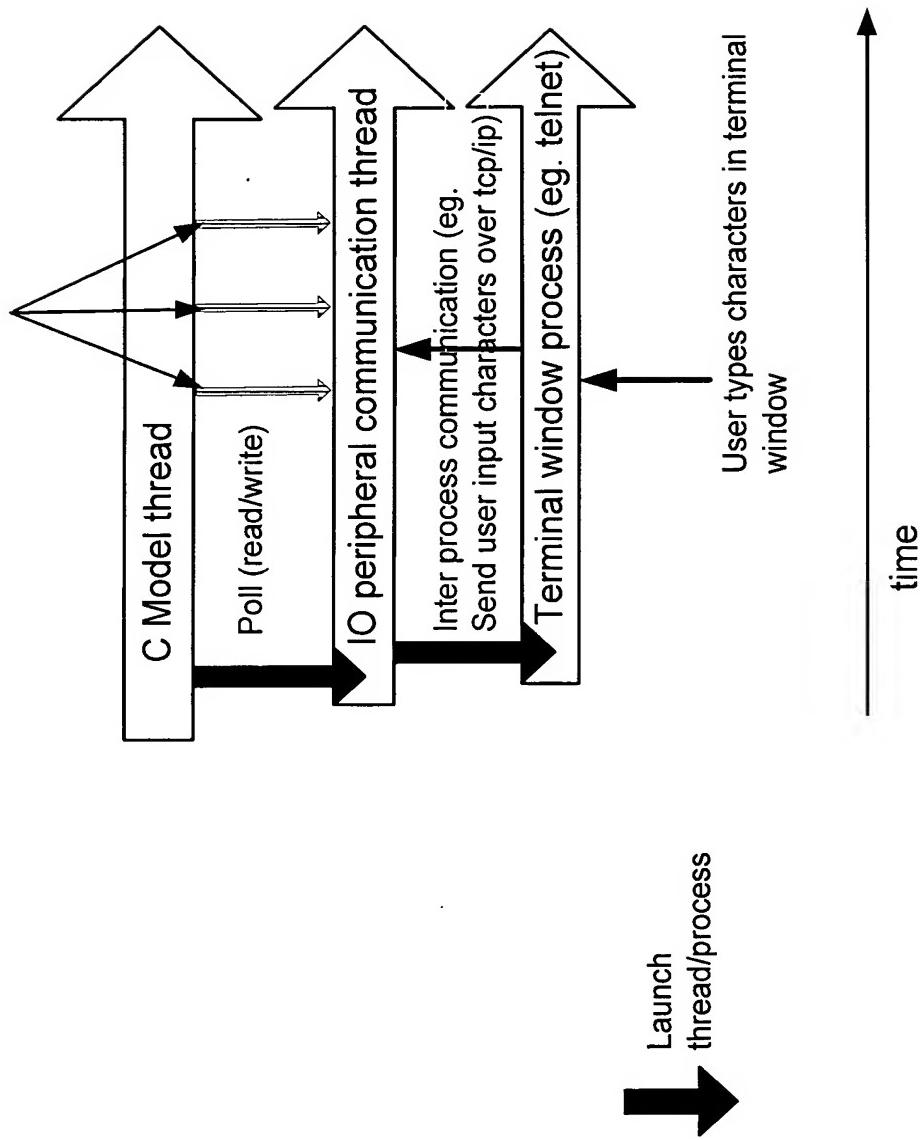


FIG. 10

